

<b>DESCRIPTION</b>			
Insulated Metal Substrate (IMS), based aluminum clad with ED copper foil on the opposite side. It is designed for the reliable thermal dissipation of circuitry. A proprietary formulated polymer-ceramic ultra thin bonding layer with high thermal conductivity and dielectric strength allows us to guarantee thermal endurance.			
The material is supplied with a film on the aluminum side to protect it against wet PCB processes.			
ROHS compliance directive 2002/95/EC and REACH N° 1907/2006			
<b>STANDARD CONSTRUCTIONS</b>			
Aluminum thickness, m (in)	1000 (0.039) – 1500 (0.059) – 2000 (0.078) – 3000 (0.12)	Aluminium Alloy / Treat	5052
Insulation thickness, m	35 micron (1,37 mils)	Dielectric thickness tolerance	± 10 m (0,4mils)
ED copper thickness, m	35 (1oz) – 70 (2oz) – 105 (3oz) - 210 (6oz)		
Other constructions available upon request			
UL Approved , QM2S2, QM2S8 File: E47820		IPC 4101	

(1) **Electrical proof test. 100% of our laminate production delivered, has been “on line” verified at 1000v V<sub>dc</sub>: 500 V/sec. ramp // 5sec.**

PROPERTIES 1500 m Al / 35 m dielectric / 70 m Cu	TEST METHOD	UNITS	TYPICAL VALUES	Guaranteed values
Time to blister at 288°C, floating on solder (50 x 50 mm)	IEC-61189	Sec	>120	>60
Copper Peel strength, after heat shock 20 sec/288°C (Cu 70mic)	IPC-TM 650-2.4.8	N/mm (Lb/in)	2,0 (16,0)	>1,5 (>10,3)
Dielectric breakdown voltage, AC (2)	IPC-TM 650-2.5.6.3	kV	3	2.5
Proof Test, DC (1)	--	V	1000	1000
Thermal conductivity (dielectric layer)	ASTM-D 5470	W/mK (W/inK)	3,20 (0,081)	3,00 (0,076)
Thermal impedance (dielectric layer) HTC 35	ASTM-D 5470	Kcm <sup>2</sup> /W (Kin <sup>2</sup> /W)	0,11 (0,017)	0,12 (0,018)
Surface resistance after damp heat and recovery	IEC-61189	MΩ	10 <sup>5</sup>	10 <sup>5</sup>
Volume resistivity after damp heat and recovery	IEC-61189	MΩm	10 <sup>4</sup>	10 <sup>4</sup>
Relative permittivity after damp heat and recovery, 10 kHz	IEC-61189	-	4,5	4,5
Dissipation factor after damp heat and recovery 10 kHz	IEC-61189	-	0,02	0,02
Comparative tracking index (CTI)	IEC-61112	V	600	>550
Permittivity	--	pF/m (pF/in)	6,7 (39,4)	6,7 (39,4)
Flammability, according UL-94, class	UL-94	class	V-0	V-0
Glass transition temperature of dielectric layer (by TMA)	IPC-TM 650-2.4.24	°C	120	120
Maximum operating temperature	--	°C	150	150

(\*) Values or parameters measured with a destructive method or limited size for the test sample must be considered as a representative values, and not as guaranteed values. They are not guaranteed over 100% of the material.

(2) **Dielectric Breakdown** test is a material destructive laboratory test. It is performed according the IPC-TM-650 part 2.5.6.3., by using AC voltage until electric failure on a relatively small surface area of the dielectric layer using metal electrodes. Values should be taken as a material reference and not as guaranteed values.

AVAILABILITY	
STANDARD SHEET SIZES mm (inch)	1220x930 (48x37), 610x460 (24x18) 1060x1170 (42x46), 1210x1000(48x40), 1025x1225 (40,3x48,2) (Also available in cut panels)
Tolerance mm (inch)	+5/-0 (+0,2/-0,0000)
Squareness mm (inch)	3 (0,1181) max., as differential between diagonal measurements.
Standard size tolerance in panels mm (inch)	+/- 0,3 (+/- 0.0118)

The data is based on typical values of standard production and should be considered as general information. Our company reserves the right to future changes. It is the responsibility of the user to ensure that the product complies with his requirements.

